



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: LAKSHMANAN, Viswanathan

Serial No.: 10/697,357

Filed: October 29, 2003

For: METHOD OF PARTITIONING AN
INTEGRATED CIRCUIT DESIGN
FOR PHYSICAL DESIGN
VERIFICATION

Art Unit: 2825

Examiner: Doan, Nghia M.

Docket No.: 03-1772 81610

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date below.

April 7, 2006 / Eric James Whitesell /
Eric J. Whitesell #38657

AMENDMENT "A" UNDER 37 C.F.R. § 1.111

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Please enter the following amendment in response to the office action mailed on
March 9, 2006.